N Channel Enhancement Mode Power MOSFET

GENERAL DESCRIPTION

The JY4N8M utilizes the latest trench processing techniques to achieve the high cell density and reduces the on-resistance with low gate charge. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

FEATURES

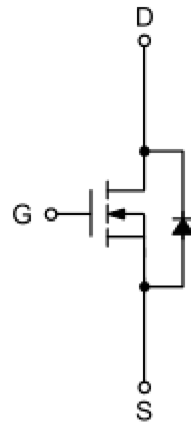
- 40V/80A, $R_{DS(ON)}$ ≤6.5mΩ@V_{GS}=10V
- Fast switching and reverse body recovery
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

APPLICATIONS

- Hard switched and high frequency circuits
- Power Management for Inverter Systems

PIN DESCRIPTION

TO-252 -2Ltop view



Absolute Maximum Ratings(Tc=25° C Unless Otherwise Noted**)**

Symbol	Parameter		Limit	Unit
V_{DS}	Drain-Source Voltage		40	V
V_{GS}	Gate-Source Voltage		±20	V
I_D	Continuous Drain Current	Tc=25° C	80	A
		Tc=100° C	55	
I_{DM}	Pulsed Drain Current		350	A
P_D	Maximum Power Dissipation		80	W
T_J T_{STG}	Operating Junction and Storage Temperature Range		-55 to +175	^{o} C
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.88	^{o} C/W
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient		92	

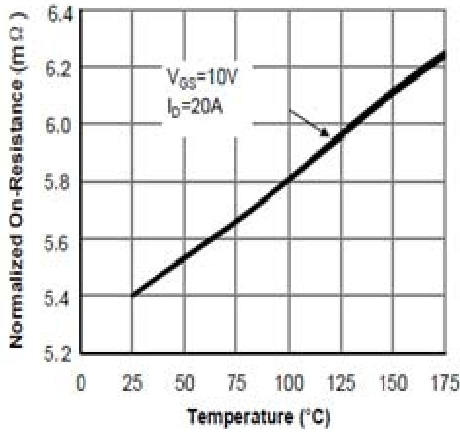
Electrical Characteristics(Ta=25° C Unless Otherwise Noted**)**

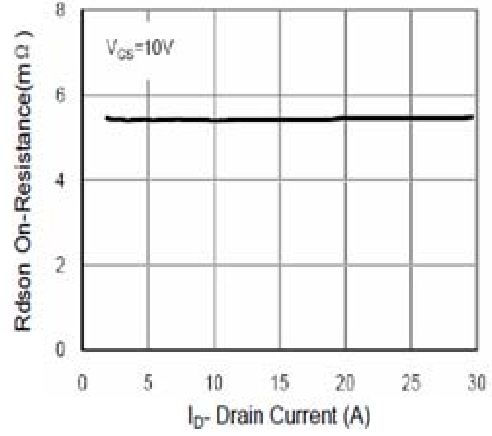
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_{DS}=250uA	40			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=40V,V_{GS}=0V			1	uA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V,V_{DS}=0V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}= $V_{GS,}$ I_{DS}=250uA	1.2	1.8	2.5	V
$R_{DS(ON)}$	Drain-Source On-state Resistance	V_{GS}=10V,I_{DS}=20A		5.4	6.5	mΩ
g_{FS}	Forward Transconductance	V_{DS}=10V, I_{DS}=20A	16			S

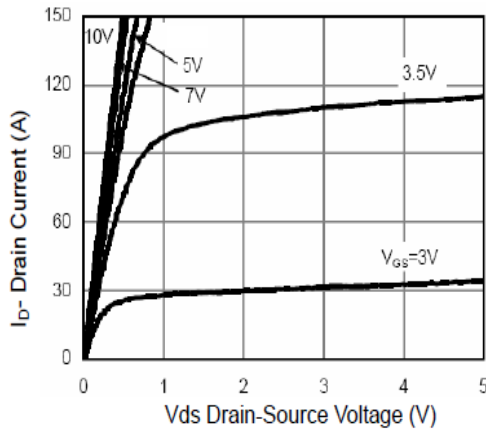
Electrical Characteristics(Ta=25ºC Unless Otherwise Noted)

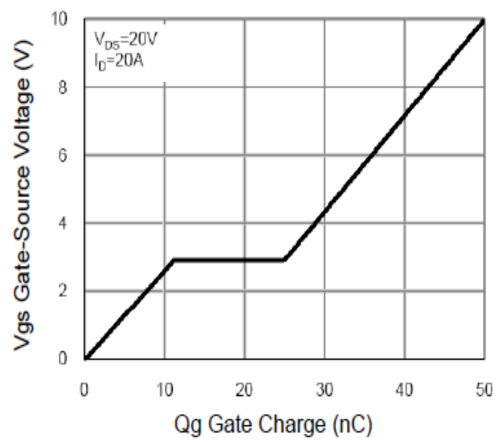
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Drain-Source Diode Characteristics						
V_{SD}	Diode Forward Voltage	V_{GS}=0V,I_{SD}=80A			1.2	V
Trr	Reverse Recovery Time	I_{SD}=40A di/dt=100A/us		30		ns
Qrr	Reverse Recovery Charge			32		nC
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V, f=1MHZ		1.2		Ω
$T_{d(on)}$	Turn-on Delay Time	V_{DS}=20V, R_G=3Ω, I_{DS} =40A, V_{GS}=10V,		12		ns
Tr	Turn-on Rise Time			10		
$T_{d(off)}$	Turn-off Delay Time			35		
T_f	Turn-off Fall Time			11		
C_{ISS}	Input Capacitance	V_{GS}=0V, V_{DS}=20V, f=1.0MHz		4005		pF
C_{OSS}	Output Capacitance			700		
C_{RSS}	Reverse Transfer Capacitance			380		
Q_g	Total Gate Charge	V_{DS}=20V,I_D=20A, V_{GS}=10V		45		nC
Q_{gs}	Gate-Source Charge			12		
Q_{gd}	Gate-Drain Charge			15		

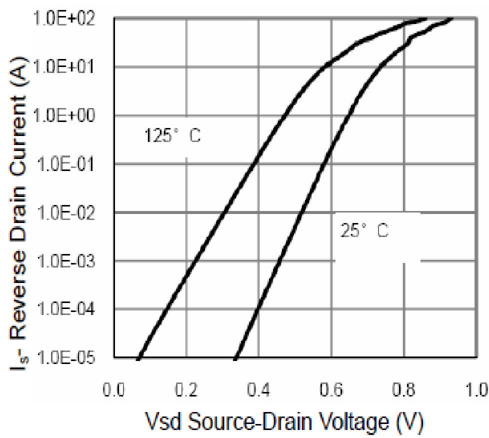
Typical electrical and thermal characteristics

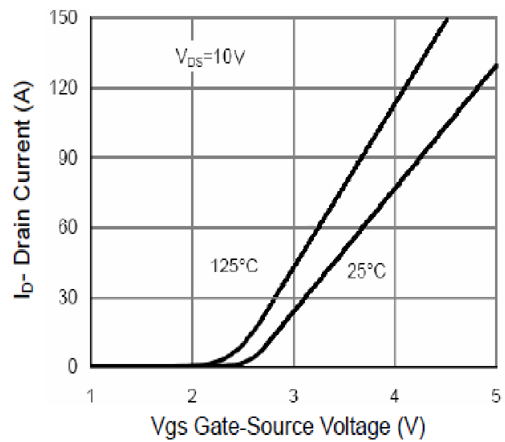












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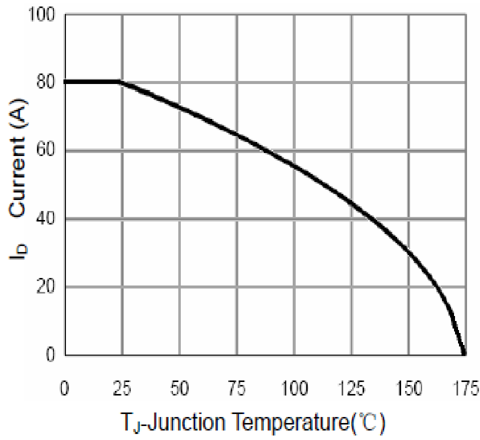
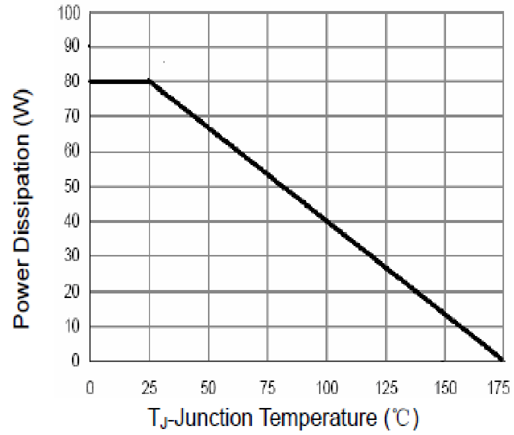
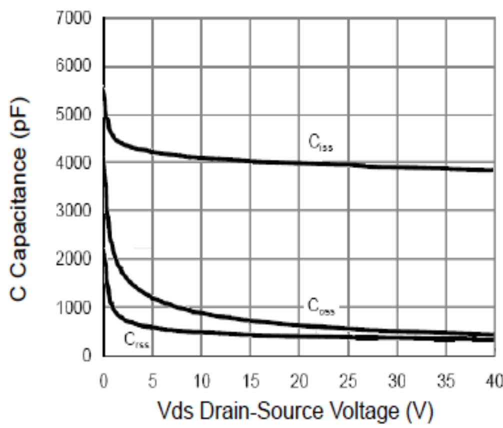
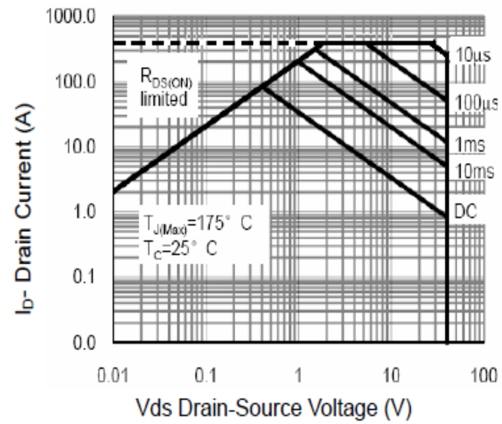
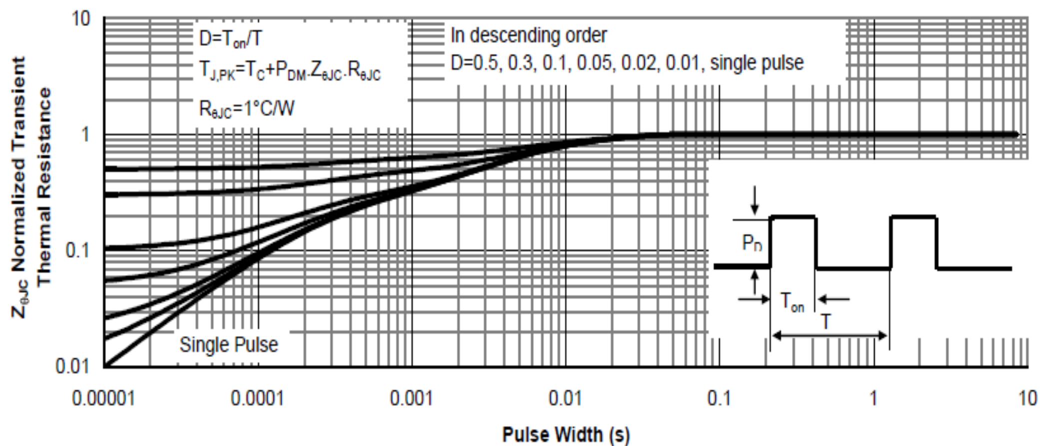


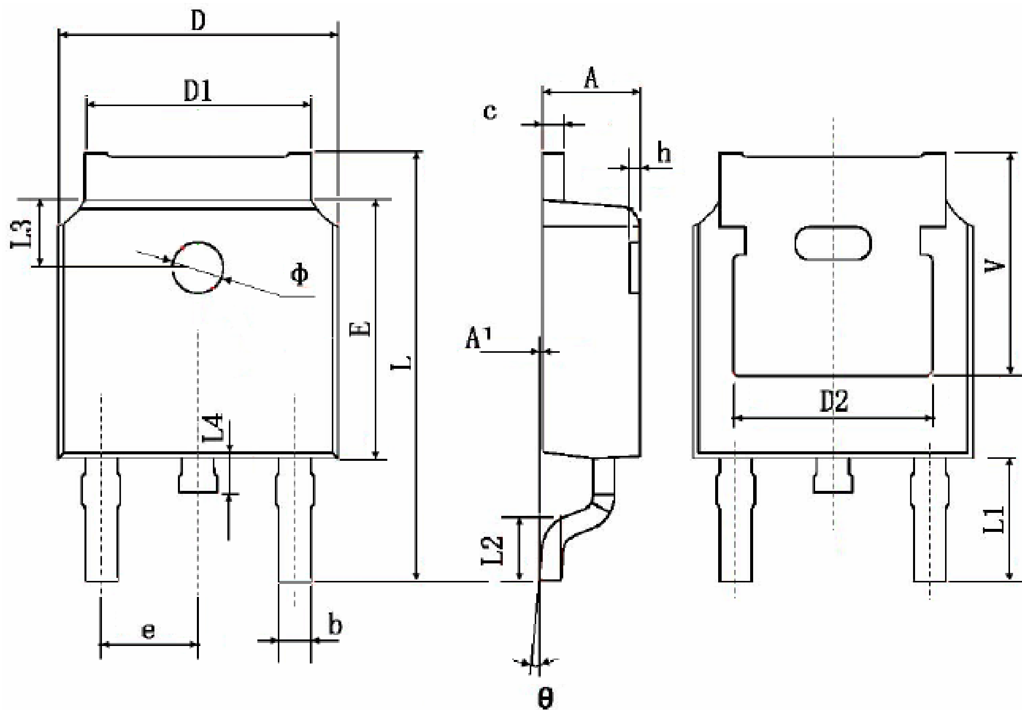








TO252 Package Outline



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	